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7590

02/18/2004

Kelly K Kordzik  
 Winstead Sechrest & Minnick PC  
 1201 Main St  
 P O Box 50784  
 Dallas, TX 75250-0784



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Serena Beller	(Depositor's name)
<i>Serena Beller</i>	(Signature)
3/12/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/032,646

12/27/2001

Mark Randolph

G0727/2243P

5662

**TITLE OF INVENTION:** PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR IMPROVED VSS RESISTANCE AND FOR PROCESS SIMPLIFICATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1330

\$0

\$1330

05/18/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
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LOKE, STEVEN HO YIN

2811

257-315000

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Winstead Sechrest  
 & Minnick P.C.

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Advanced Micro Devices,, Inc.

Sunnyvale, CA 94086

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) *Kelly K. Kordzik* (Date) 3-12-04  
 Kelly K. Kordzik, Reg. No. 36,571

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03/17/2004 MAHMEED2 00000149 010365 10032646  
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